

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

1. (currently amended) An electrically addressable device for recording, addressing, and reading of data, comprising:

a storage array unit having multiple layers of data storage medium, each of said layers being mounted on a substrate;

an electrical marking device associated with at least one of the multiple layers of data storage medium of the storage array unit to provide a display indicating pre-selected information; and

at least one display layer of the multiple layers of data storage medium, said display layer being partially visually alterable to provide said display; and

the pre-selected information comprising information about ~~the~~ at least one subject matter and name of the content of the data.

2. Cancelled.

3. (previously presented) The electrically addressable device as recited in claim 1, wherein the display layer further comprises a plurality of multiple-state information storage cells each representing a value of at least one data bit, wherein the visual appearance of each information storage cell varies depending on the state of the information storage cell..

4. (previously presented) The electrically addressable device as recited in claim 1, wherein the information storage cells each further comprising a multiple state electrical device which changes states depending on the value of the data bit and having variable visual appearance depending on the state of the electrical device.

5. (original) The electrically addressable device as recited in claim 4, wherein the electrical device includes an electrical fuse that has a modulated visual appearance, depending on whether the fuse has gone to an open circuit.

6. (original) The electrically addressable device as recited in claim 5, wherein the electrical fuse includes a visual marker that is activated to change the visual appearance of the electrical fuse when the fuse is blown.

7. (previously presented) The electrically addressable device as recited in claim 1, wherein the display layer comprises an outermost layer of the storage array unit.

8. (original) The electrically addressable device as recited in claim 7, wherein the substrate for the display layer is substantially opaque.

9. (previously presented) The electrically addressable device as recited in claim 7, and further comprising a reflective layer between the display layer and a next layer in the storage array unit.

10. (original) The electrically addressable device as recited in claim 3, wherein the visual appearance of each information storage cell is changed by varying the opacity of the information storage cell.

11. (original) The electrically addressable device as recited in claim 3, wherein the visual appearance of each information storage cell is changed by varying the reflectivity of the information storage cell.

12. (original) The electrically addressable device as recited in claim 3, wherein the visual appearance of each information storage cell is changed by varying the color of the information storage cell.

13. (original) The electrically addressable device as recited in claim 1, wherein the pre-selected information indicates the nature of the content of the data stored on the storage array unit.

14. Cancelled

15. Cancelled

16. Cancelled

17. Cancelled

18. (previously presented) An electrically addressable device for recording, addressing and reading of data, comprising:

a storage array unit having multiple layers of data storage medium, each layer comprising a plurality of bi-state electrical devices arranged in orthogonal matrix and a plurality of conductors provided in a substantially orthogonal relationship on each layer;

a plurality of substrates on which the layers are disposed; and

an electrical marking device on at least one of the layers of storage medium of the storage array unit comprising a plurality of the bi-state electrical devices disposed to provide a display indicating pre-selected information, depending on the state of each of the electrical devices, and wherein the electrical marking device comprises an addressing device for storing the data on the storage array unit sequentially across each layer of the storage array unit, wherein the display indicates the amount of storage array unit that has been recorded with data.

19. (original) The electrically addressable device as recited in claim 18, wherein each of the electrical bi-state devices comprises a write-once device.

20. (previously presented) The electrically addressable device as recited in claim 18, wherein the display indicates information about the nature of the content of data stored on the storage array unit.

21. Cancelled

22. Cancelled

23. (original) The electrically addressable device as recited in claim 18, wherein each of the electrical bi-state devices comprises a fuse device.

24. (currently amended) A method for marking the content of an electrically addressable device used for recording, addressing and reading of data, and having a storage array unit with multiple layers of data storage medium, each mounted on a substrate, comprising:

selecting at least one outermost layer of data storage medium as a display layer;

disposing a reflective coating at an interface of the display layer and remaining layers of the storage array unit;

electrically storing data on the outermost layer to provide a display indicating pre-selected information; and

electrically addressing the device for storing the data on the storage array unit sequentially across each layer of the storage array unit, wherein the display indicates the amount of storage array unit that has been recorded with data.

25. Cancelled

26. (original) The method of marking as recited in claim 24, wherein the display indicates information about the content of the data.

27. Cancelled

28. (currently amended) The electrically addressable device as recited in claim ~~24~~ 18, wherein the storage array device has corresponding data addresses at an approximately ~~the same~~ similar relative location on each layer of the storage array unit and wherein the addressing device

simultaneously stores data on multiple layers of the storage array unit at approximately the same relative location on each layer.

29. (previously presented) The electrically addressable device as recited in claim 28, and further comprising a reflective layer on at least one of the outermost layers of the storage array unit.

30 (previously presented) The electrically addressable device as recited in claim 29, and further comprising a first reflector disposed above a plurality of pre-selected layers of data storage medium and a second reflector disposed below the pre-selected layers of data storage medium.